

# Online Library Cadence Tempus Manual

## **Cadence Tempus Manual**

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## **Cadence Tempus Manual**

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The Cadence ® Tempus ™ Timing Signoff Solution is the industry's most trusted static timing analysis (STA) tool for FinFET designs. It is the fastest STA tool in the industry today with unique distributed processing and cloud capabilities scaling to hundreds of CPUs to quickly complete even the largest designs.

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## **Tempus Timing Signoff Solution - Cadence**

The Cadence®Tempus™ Timing Signoff Solution is the fastest static timing analysis (STA) tool in the industry today with unique distributed processing and cloud capabilities enabling hundreds of CPUs to quickly complete even the

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largest designs.

## **Tempus Timing Signoff Solution - Cadence**

Length : 2 days This course is a detailed exploration of the Tempus™ Timing Signoff Solution, which supports distributed processing and enables fast static timing analysis with full signal

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integrity (SI) and glitch analysis, statistical variation (SOCV), and Multi-Mode and Multi-Corner (MMMC) analysis. In this course, you analyze a design for static timing and signal integrity issues, test ...

## **Tempus Signoff Timing Analysis and Closure - Cadence**

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The Tempus Power Integrity Solution is the result of an integration between the widely used Cadence Tempus Timing Signoff Solution and the Voltus™ IC Power Integrity Solution. Using the new tool, customers can significantly lower IR drop design margins without sacrificing signoff quality, thus improving power and area.



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## **Cadence Announces Tempus Power Integrity Solution for ...**

A new common user interface that the Genus synthesis solution shares with Cadence Innovus™ Implementation System and Cadence Tempus™ Timing Signoff Solution streamlines flow development and simplifies usability

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across the complete Cadence digital flow.

## **Genus Synthesis Solution - Cadence Design Systems**

User Manual SPECULAR TEMPUS  
(Firmware V3.8) 7 2.8 Tap Tempo In  
Specular Tempus tap-tempo can be  
accomplished in 3 ways : 1. On the pedal

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itself, using footswitch B as tap-tempo switch. 2. Using external aux switch box (see Chapter 4). 3. Using MIDI controller.

## **User Manual SPECULAR TEMPUS - GFI System Webpage**

Voltus/Tempus IC Power Integrity  
Solution Important! You will need to  
read, fill out and agree to the Cadence

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EULA before you can utilize any Cadence software:

## **Voltus/Tempus IC Power Integrity Solution**

User Manual SPECULAR TEMPUS

Firmware V3.7 \* Important - must read \*

1 Chapter 1 - OVERVIEW Specular

Tempus is a powerful reverb and delay

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effect processor in a compact pedal. Featuring 32 hand-crafted algorithms (13 reverbs, 13 delays, and 6 reverb & delay combinations), 8 presets. Stereo in/out, tap-tempo, and extensive inter-connectivity.

**Specular Tempus - User Manual (en)  
- v3**

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As part of the collaboration, the Cadence® digital, signoff and custom/analog tools have been certified for Design Rule Manual (DRM) and SPICE v1.0, and Cadence IP has been enabled for the TSMC 5nm process.

**Cadence**  **5nm**  
**FinFET**  **SoC** 

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Tempus is a technology company that has built the world's largest library of clinical and molecular data and an operating system to make that information accessible and useful for patients, physicians and researchers.

## **About Us - Tempus**

Cadence Academic Network. ... Genus,

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Innovus, Tempus all have common timing engines. Genus and Innovus have common placement engines, and common global routers. And so on for the other engines. It is good to have a rough (over-) simplified view of how synthesis is actually done.

**Genus and Innovus: Together at**



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## **Last - Cadence Community**

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